

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NUM. 10073220 FILING DATE 02/13/2002 CLASS 323 SUBCLASS 245 GAU 7817 EXAMINER GLENN [Signature]

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**CONTINUING DATA VERIFIED:

BEST AVAILABLE COPY

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-382033 12/14/2000

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no		027260-514
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiner's initials			
TITLE : DC block circuit and communication equipment			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436 (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.O.
Assistant Examiner		DRAWING	
		Sheets Drawn	Fig.s Drawn
Primary Examiner		Print Fig.	
		Application Examiner	
PREPARED FOR ISSUE			
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DISCLAIMER		(Attached in pocket on right inside flap)	